

FORM PTO-1449 (REV. 7-80)		U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE		ATTY. DOCKET NO. 501214.02 (30204/US/2)		APPLICATION NO. 10/815,890 Not Yet Assigned	
INFORMATION DISCLOSURE STATEMENT <i>(Use several sheets if necessary)</i>				APPLICANT(S) Howard C. Kirsch			
				FILING DATE 3/30/04 Concurrently herewith		GROUP ART UNIT 28/8 Not Yet Assigned	

U.S. PATENT DOCUMENTS							
*EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE
TP	AA	5,608,668	03/04/97	Zagar	365	149	
	AB	5,844,833	12/01/98	Zagar et al.	365	149	
	AC	5,926,410	07/20/99	Raad et al.	365	63	
	AD	6,043,562	04/28/00	Keeth	257	776	
	AE	6,515,925 B2	02/04/03	Graham et al.	365	205	
	AF	6,535,439 B2	03/18/03	Cowles	365	201	
	AG	6,549,476 B2	04/15/03	Pinney	365	190	
↓	AH	6,566,206 B2	05/20/03	Clampitt	438	296	
	AI						
	AJ						

FOREIGN PATENT DOCUMENTS								
		DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION	
							YES	NO
	AK							
	AL							
	AM							
	AN							
	AO							

OTHER PRIOR ART <i>(Including Author, Title, Date, Pertinent Pages, Etc.)</i>	
AP	Sekiguchi, T. et al., "A Low-Impedance Open-Bitline Array for Multigigabit DRAM", IEEE Journal of Solid-State Circuits, Vol. 37, No. 4, April 2002, pp. 487-498.

EXAMINER <i>Trong Phan</i>	DATE CONSIDERED <i>7/7/04</i>
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* EXAMINER: Initial if reference considered, whether or not criteria is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant(s).